

PATENT

Attorney Docket:
33216M549654

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

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| In re Patent Application of: |) | |
| |) | |
| Kaoru Ishida, et al. |) | Examiner: To Be Assigned |
| |) | |
| Serial No. : To Be Assigned |) | Group Art Unit: To Be Assigned |
| |) | |
| Filed: Herewith |) | |

For : POWER SPLITTER/COMBINER MULTI-LAYER CIRCUIT

LETTER TO EXAMINER REGARDING PRIORITY

Commissioner For Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

Applicant is filing a divisional application concurrently herewith. With this filing, Applicant is attaching a copy of the executed Declaration/Power of Attorney. However, regarding priority for this application, only Japanese Application No. Hei 9-252,419, filed September 17, 1997 should be claimed.

Respectfully submitted,
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